

TITLE: B3 HM185WX3-200

Product Specification

Rev. O

HEFEI BOE OPTOELECTRONICS TECHNOLOGY

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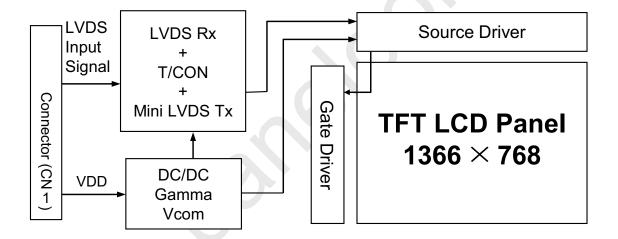
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1.0 GENERAL DESCRIPTION

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1.1 Introduction

HM185WX3-200 is a color active matrix TFT LCD open-cell using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This open-cell has a 18.5 inch diagonally measured active area with WXGA resolutions (1366 horizontal by 768 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this open-cell can display 16.7M colors. The TFT-LCD panel used for this open-cell is adapted for a low reflection and higher color type.



1.2 Features

- LVDS Interface with 1 pixel / clock
- High-speed response
- Low power consumption
- 6-bit (Hi-FRC) color depth, display 16. 7M colors
- Low reflection and normal viewing angle
- DE (Data Enable) only
- RoHS



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1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

The followings are general specifications at the model HM185WX3-200.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	$409.8(H) \times 230.4(V)$	mm	
Number of pixels	1366(H) ×768(V)	pixels	
Pixel pitch	$0.3(H) \times 0.3(V)$	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.7M	colors	
Display mode	Normally White		
Weight	500 (Max.)	g	
Surface Treatment	Haze 25%, 3H		



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2.0 ABSOLUTE MAXIMUM RATINGS

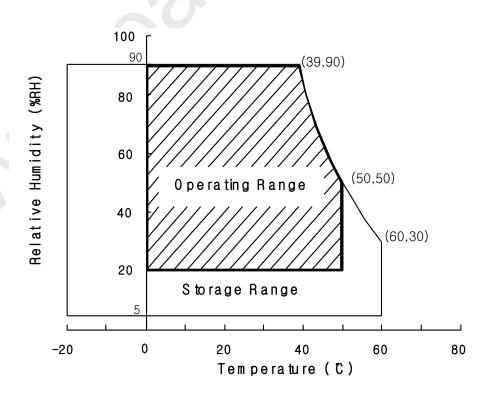
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The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings> [VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-0.5	5.5	V	
Logic Supply Voltage	V _{IN}	VSS-0.3	V _{DD} +0.3	V	Ta = 25 °C
Operating Temperature	T_{OP}	0	+50	$^{\circ}\!\mathbb{C}$	1)
Storage Temperature	T_{ST}	-20	+60	$^{\circ}$ C	1)

Note: 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.







3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical specifications >

 $[Ta = 25 \pm 2 \degree C]$

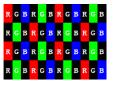
Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	4.5	5.0	5.5	V	N-4-1
Power Supply Current	I_{DD}	-	600	1000	mA	Note1
In-Rush Current	I_{RUSH}	-	2.0	3.0	A	Note 2
Permissible Input Ripple Voltage	V_{RF}	-	-	100	mV	$V_{\rm DD} = 5.0 \mathrm{V}$
High Level Differential Input Threshold Voltage	V _{IH}	-	-	+100	mV	
Low Level Differential Input Threshold Voltage	V _{IL}	-100	-	-	mV	
Differential input voltage	V _{ID}	200	-	600	mV	
Differential input common mode voltage	Vcm	1.0	1.2	1.5		V_{IH} =100mV, V_{IL} =-100mV
Power Consumption	P_{D}	A-	3	4.5	W	

Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=5.0V, Frame rate=75Hz and Clock frequency = 95MHz. Test Pattern of power supply current

a) Typ: Color Bar pattern

b) Max: Skip Sub Pixel Pattern



2. Duration of rush current is about 2 ms and rising time of VDD is 520 μ s \pm 20 %



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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25±2°C) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta_{\emptyset=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\emptyset=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\emptyset=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{\varnothing=270} (=\theta_6)$ as the 6 o'clock direction ("bottom"). While scanning θ and/or \varnothing , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 5.0V, Frame rate = 60Hz, Clock = 78MHz, I_{BL} = 7.5mA, Ta =25 \pm 2 °C]								
Parame	ter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
	TT 2 4 - 1	Θ_3		35	45	-	Deg.	
Viewing Angle range	Horizontal	Θ_9	CR > 10	35	45	-	Deg.	
	X7 .: 1	Θ_{12}	CR > 10	20	25	-	Deg.]
	Vertical	Θ_6		35	40	-	Deg.	Note 1
	II - ni nt - 1	Θ_3		50	-	-	Deg.	Note 1
X7::	Horizontal	Θ_9	CD > 5	50	-	-	Deg.]
Viewing Angle range	Vertical	Θ_{12}	CR > 5	30	-	-	Deg.]
	verticai	Θ_6		45	-	-	Deg.	
Luminance Contrast ratio		CR		450	600			Note 2
Luminance of White		$Y_{\rm w}$		160	200		cd/m ²	Note 3
White luminance unif	formity	ΔΥ		75	80		%	Note 4
	White	W_{x}		0.283	0.313	0.343		
	Wille	W_y	$\Theta = 0^{\circ}$ (Center)	0.299	0.329	0.359		
	Red	R_x	Normal	0.616	0.646	0.676		
Reproduction	Keu	R_y	Viewing Angle	0.304	0.334	0.364		Note 5
of color	Cmaam	G_{x}	8	0.266	0.296	0.326		Note 3
	Green	G_{y}		0.572	0.602	0.632		
	Dluc	B_x		0.114	0.144	0.174		
	Blue	\mathbf{B}_{y}		0.038	0.068	0.098		
Response	Rising	$T_{\rm r}$			1.5	2.5	ms	Note 6
Time	Falling	T_{f}			3.5	5.5	ms	INOIE 0
Cross Ta	ılk	CT		-	-	2.0	%	Note 7

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Note:

- 1. The value in upper table are based on BLU provided by BOEHF.
- 2. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 3. Contrast measurements shall be made at viewing angle of θ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

CR = Luminance when displaying a white raster

Luminance when displaying a black raster

4. Luminance of LCD module shall be made without signal input. Cell transmittance is defined mathematically, BLU provided by BOEHF.

> Luminance of LCD Module Transmittance Luminance of BLU

- The White luminance uniformity on LCD surface is then expressed as: 5. $\Delta Y = (Minimum Luminance of 9points / Maximum Luminance of 9points) * 100$ (See FIGURE 2 shown in Appendix).
- 6. The color chromaticity coordinates specified in Table 4. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel with BLU provided by BOEHF.
- The electro-optical response time measurements shall be made as FIGURE 3 shown in 7. Appendix by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td, and 90% to 10% is Tr.
- 8. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_R) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).



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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

• CN1 Open-cell Side Connector : UJU IS100-30O-C23 or Equivalent User Side Connector : JAE FI-X30H or Equivalent

Pin No	Symbol	Function	Remark
1	NC	No connection	
2	CE	No connection	internal use
3	CTL	No connection	internal use
4	GND	GND Ground	
5	RX0-	Negative LVDS differential data input. Channel 0	
6	RX0+	Positive LVDS differential data input. Channel 0	
7	GND	Ground	
8	RX1-	Negative LVDS differential data input. Channel 1	
9	RX1+	Positive LVDS differential data input. Channel 1	
10	GND	Ground	
11	RX2-	Negative LVDS differential data input. Channel 2	
12	RX2+	Positive LVDS differential data input. Channel 2	
13	GND	Ground	
14	RXCLK-	Negative LVDS differential clock input.	
15	RXCLK+	Positive LVDS differential clock input.	
16	GND	Ground	
17	RX3-	Negative LVDS differential data input. Channel 3	
18	RX3+	Positive LVDS differential data input. Channel 3	
19	GND	Ground	
20	NC	Not connection, this pin should be open.	
21	NC	Not connection, this pin should be open.	
22	NC	Not connection, this pin should be open.	
23	GND	Ground	
24	GND	Ground	
25	GND	Ground	
26	VCC	5V Power supply	
27	VCC		
28	VCC		
29	VCC		
30	VCC	7	



5.2 LVDS Interface (Tx; THC63LVDF83A or Equivalent) 5.2.1 LVDS Interface

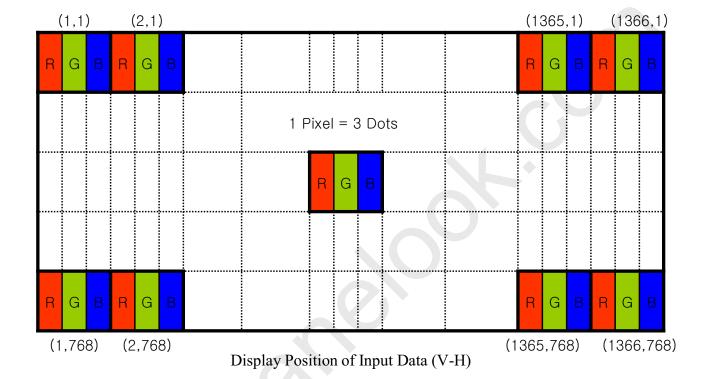
	Input	Trans	mitter	Inter	face	HM185WX3-200 (CN11)	Remark
	Signal	Pin No.	Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.	
	OR0	51					
	OR1	52					
	OR2	54	40	OUT0-	RXO0-		
	OR3	55	48 47	OUT0+	RXO0+	$\stackrel{1}{\diamond}$ 2	
	OR4	56	.,		10100	2	
	OR5	3					
	OG0	4					
	OG1	6					
	OG2	7		OLUTI	DVO1	2	
	OG3	11	4.6				
	OG4	12	46 45	OUT1- OUT1+	RXO1- RXO1+	3 4	
	OG5	14	15	John	101011	'	
	OB0	15					
	OB1	19					
L V	OB2	20					
Ď	OB3	22		OUT2- OUT2+		5 6	
S	OB4	23	42		RXO2- RXO2+		
	OB5	24	42 41				
	Hsync	27	71	00121			
	Vsync	28					
	DE	30					
	MCLK	31	40	CLK OUT-	RXO CLK-	8	
			39	CLK OUT+	RXO CLK+	9	
	OR6	50					
	OR7	2					
	OG6	8	38	OUT3-	RXO3-	10	
	OG7	10	37	OUT3+	RXO3+	11	
	OB6	16					
	OB7	18					
	RSVD	25					



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5.3 Data Input Format

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6.0 SIGNAL TIMING SPECIFICATION

6.1 The HM185WX3-200 is operated by the DE only.

Item		Symbols	Min	Тур	Max	Unit
	Frequency	1/Tc	50	78	95	MHz
Clock	High Time	Tch	-	4/7Tc	C	
	Low Time	Tcl	-	4/7Tc		
			778	806	888	lines
F1	rame Period	Tv	50	60	75	Hz
			20	16.7	13.3	ms
Vertica	al Display Period	Tvd		768	-	lines
One line Scanning Period		Th	1446	1560	1936	clocks
Horizon	tal Display Period	Thd	-	1366	-	clocks



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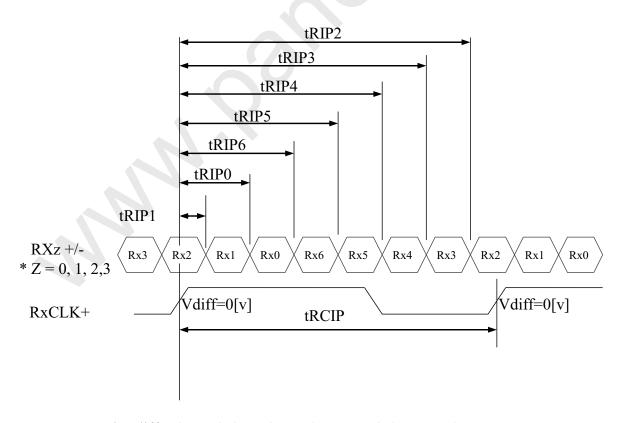
6.2 LVDS Rx Interface Timing Parameter

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The specification of the LVDS Rx interface timing parameter is shown in Table 4.

<Table 4. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
CLKIN Period	tRCIP	10.60	13.25	20.00	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	2 ×tRCIP/7-0.4	2 ×tRCIP/7	$2 \times tRCIP/7+0.4$	nsec	
Input Data 3	tRIP5	3 ×tRCIP/7-0.4	3 ×tRCIP/7	$3 \times tRCIP/7+0.4$	nsec	
Input Data 4	tRIP4	4 ×tRCIP/7-0.4	4 ×tRCIP/7	$4 \times tRCIP/7 + 0.4$	nsec	
Input Data 5	tRIP3	5 ×tRCIP/7-0.4	5 ×tRCIP/7	$5 \times tRCIP/7+0.4$	nsec	
Input Data 6	tRIP2	6 ×tRCIP/7-0.4	6 ×tRCIP/7	$6 \times \text{tRCIP/7+0.4}$	nsec	



* Vdiff = (RXz+)-(RXz-),...,(RXCLK+)-(RXCLK-)

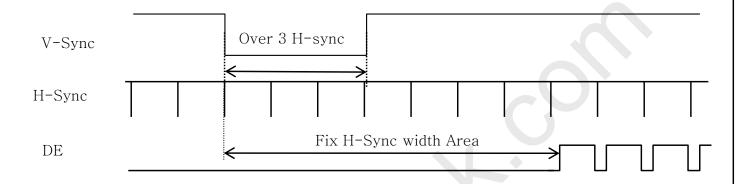


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7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

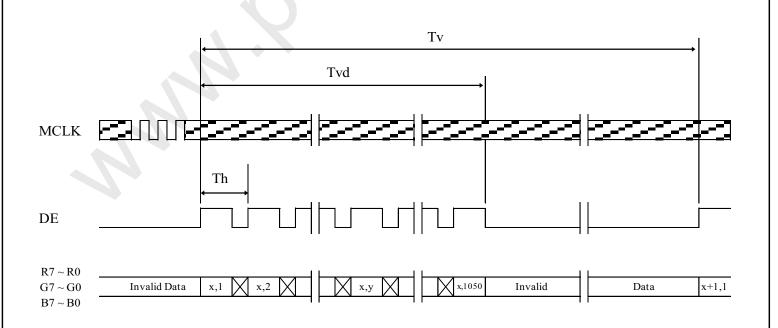
7.1 Sync Timing Waveforms

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- 1) Need over 3 H-sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

7.2 Vertical Timing Waveforms

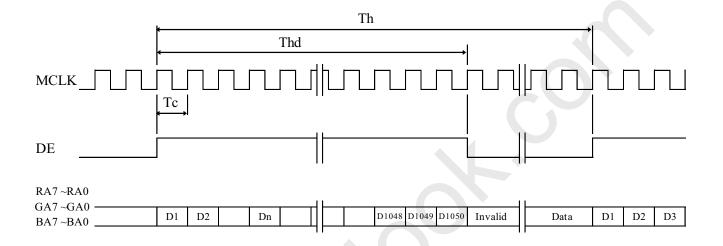


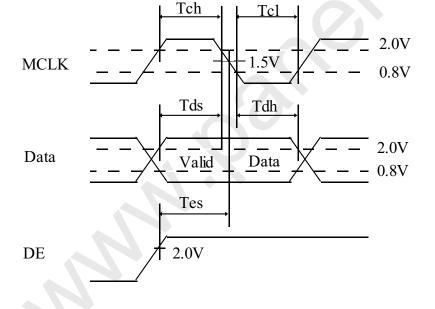


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7.3 Horizontal Timing Waveforms

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8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

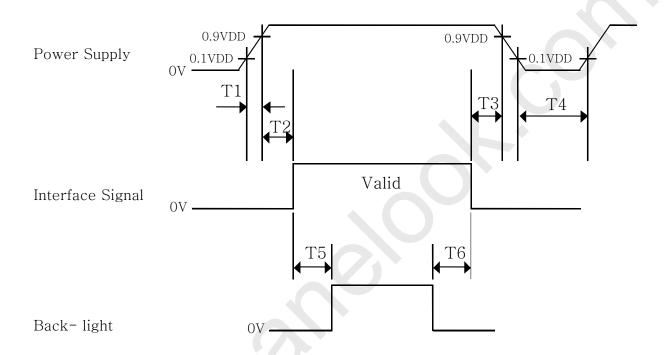
C 1 0 6	2 0 1	RED DATA								GREEN DATA						BLUE DATA									
Color & C	ray Scale	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	B4	В3	B2	В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic Colors	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	\triangle				•	<u> </u>															•	<u> </u>			
of RED	∇					ļ							1.									ļ			
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	0	Ō	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Gray Scale	\triangle				•	\uparrow				<u> </u>						<u> </u>									
of GREEN	∇																					<u> </u>			
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	∇	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Gray Scale	Δ					<u> </u>								<u> </u>								<u> </u>			
of BLUE	∇					ļ																<u> </u>			
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
Gray Scale	\triangle					<u> </u>								<u> </u>								<u> </u>			
of WHITE	∇					<u> </u>							,	_								<u> </u>			
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1



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9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD open-cell, the power on/off sequence shall be as shown in below



- \bullet 0.5 ms \leq T1 \leq 10 ms
- \bullet 0 \leq T2 \leq 50 ms
- \bullet 0 \leq T3 \leq 50 ms
- \bullet 1 sec \leq T4
- \bullet 200 ms \leq T5
- \bullet 200 ms \leq T6

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.



10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model HM185WX3-200. Other parameters are shown in Table 5.

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Weight	500 (max.)	gram
Active area	$409.8(H) \times 230.4(V)$	mm
Pixel pitch	$0.3(H) \times 0.3(V)$	mm
Number of pixels	$1366(H) \times 768(V)$ (1 pixel = R + G + B dots)	pixels

10.2 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.



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11.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

<Table 6. Reliability Test Parameters >

No	Test Items	Conditions		
1	High temperature storage test	Ta = 60 °C, 240 hrs		
2	Low temperature storage test	$Ta = -20 ^{\circ}\text{C}, 240 \text{hrs}$		
3	High temperature & high humidity (operation test)	Ta = 50 °C, 80%RH	I, 240hrs	
4	High temperature operation test	$Ta = 50 ^{\circ}\text{C}, 240 \text{hrs}$		
5	Low temperature operation test	$Ta = 0 ^{\circ}C, 240 hrs$		
6	Thermal shock	Ta = -20 °C \leftrightarrow 60 °C (0.5 hr), 100 cycle		Note 1
7	Electro-static discharge test	Air: 150 pF, 330Ω, 15 KV		
/	(non-operating)	Contact: 150 pF, 330Ω, 8 KV		
	Vibration test	Frequency	10 ~ 300 Hz, Sweep rate 30 min	
8	(non-operating)	Gravity / AMP	1.5 G	
		Period	+X, +Y, +Z 30 min	
9	Paglzing Vibration Tost	1.47Grms, 1~200Hz, Random		
9	Packing Vibration Test	$\pm X$, $\pm Y$, $\pm Z$ per 1hr		Note 2
10	Dron Toot	1Angle,3Edge,6Face		
10	Drop Test	Height: JIS-Z-0200 Level 1		

Notes:

- 1. The tests are done with LCD modules. (Use BOEHF BLU)
- 2. The test is done with a package (20pcs open cell / 1 Box) shown in section 14.



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12.0 HANDLING & CAUTIONS

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- (1) Cautions when taking out the open cell
 - Pick the pouch only, when taking out open cell from a shipping package.
- (2) Cautions for handling the open cell
 - As the electrostatic discharges may break the LCD open cell, handle the LCD open cell with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel is made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD open cell is operating.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the open cell is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the open cell would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD open cell in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the open cell characteristics
 - Do not apply fixed pattern data signal to the LCD open cell at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not re-adjust variable resistor or switch etc.
 - When returning the open cell for repair or etc., Please pack the open cell not to be broken. We recommend to use the original shipping packages.

Hex-Decimal

00000-FFFFFF





13.0 PRODUCT SERIAL NUMBER



2 3 6 7 5 X X X X X X X X X X X X X X X X X Digit 2 5 7 8 15 10 11 12 13 14 16 17 Code 0 S 3 9 3 5 2 0 0 0 Code S D В Gra Model Lin Year Мо Model Extension Serial No

Code

FGCOD)

(Last 4 Digits Of

nth

Line		
Code	Description	
3	BOEHF	

Code

/GBN

Descriptio

de

Month		
Code	Description	
1	1月	
2	2月	
•••		
Α	10月	
В	11月	
С	12月	

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14.0 Packing

14.1 Packing Order

-. First put one PE cushion in the tray

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-. Then put one open cell on PE cushion



-. One PE cushion again,, Totally 4 pcs open cell and 5 pcs PE cushion in one tray, Finally another 4mm epe on the top.



-. One pallet contains 12 boxes, that is 240 pcs open cell (outbox not shown);



-. Totally 6 tray (one empty tray on the top, 20pcs open cell) in PE bag, PE bag sealing by tape;



-. Top cover

A4(210 X 297)

Attention:

- Open cell packing in clean room; 1.
- Operators should take open cell careful, especially COF; 2.
- 3. 20pcs open cell must be filled in one paper box;
- 4. Packing flow should be strictly followed;
- 5. Open cell is fragile materials, please pay attention both in packing and transportation;

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-. Tape sealing

(20 pcs open cell in one box)



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14.2 Packing Note

• Box Dimension : $440\text{mm}(W) \times 550\text{mm}(L) \times 220\text{mm}(H)$

• Package Quantity in one Box : 20pcs

14.3 Box label

• Label Size : 108 mm (L) × 56 mm (W)

Contents

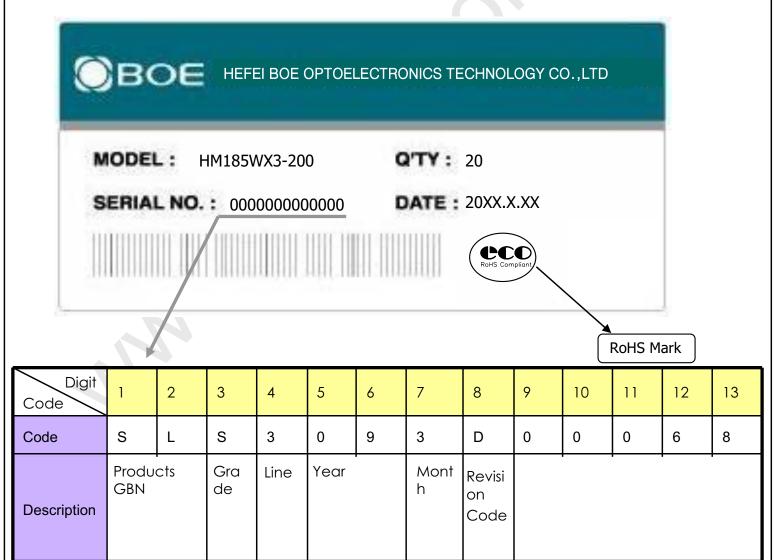
Open cell: HM185WX3-200

Q'ty: 20

Serial No.: Box Serial No. See following picture for detail description.

Date : Packing Date

FG Code: FG Code of Product



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15.0 APPENDIX

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Figure 1. Measurement Set Up

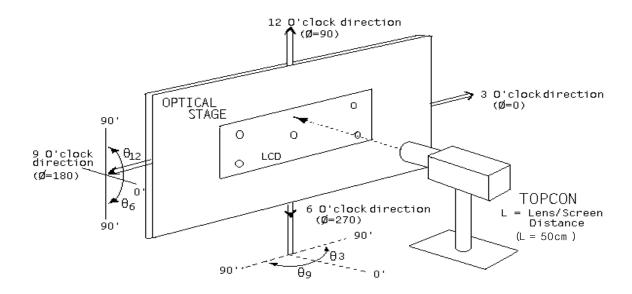


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)

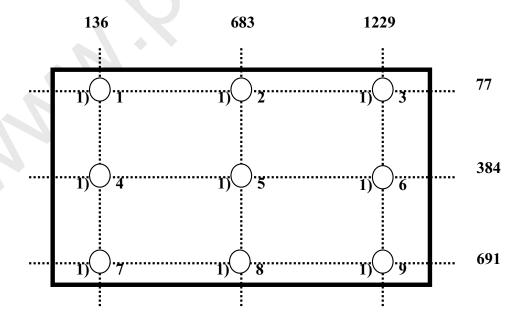






Figure 3. Response Time Testing

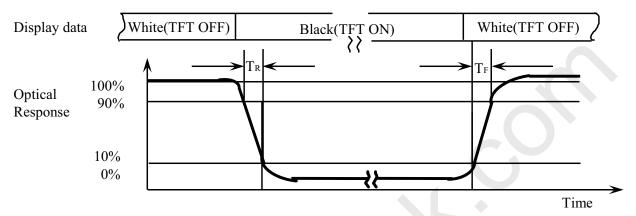
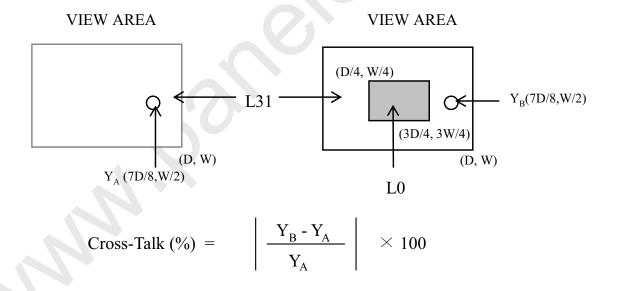


Figure 4. Cross Modulation Test Description



Where: $Y_A = Initial luminance of measured area (cd/m^2)$ $Y_B = Subsequent luminance of measured area (cd/m^2)$ The location measured will be exactly the same in both patterns



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Figure 5. Open-cell Outline Dimensions

